

# DDC\_U v125

- Provides 16 fully tunable BBCs for each IF
- Selectable bandwidth of 2, 4, 8, 16, 32, 64 and 128 MHz for each BBC
- Multicast support is implemented, sending out a packet with all status information for the DBBC3 each second
- The latest firmware version (dbbc3\_ddc\_U\_v125-2hv2\_071020\_1.bit) has greatly improved stability.  
This leads to less time for the initial phase synchronization and more sync stability even at higher system temperatures (up to 70°C FPGA temperature).
- The latest version of the control software (April 19th 2021) has several bugfixes improving initial synchronization and communication with the GcoMos and Core3H.
- New Version will be released soon on <https://www.hat-lab.cloud/>